IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Art Unit:

Not assigned

Terunao HANAOKA

Examiner: Not assigned

Serial No:

Not assigned

Filed: March 17, 2004

For:

Semiconductor Device and Method for Manufacturing the

Same, Circuit Board and Electronic Equipment

TRANSMITTAL OF INFORMATION DISCLOSURE **STATEMENT**

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The information disclosure statement submitted herewith is being filed concurrently with the subject application [37 C.F.R. § 1.97(b)] and contains no items of information cited in any communication from a foreign patent office in a counterpart foreign application [37 C.F.R. § 1.97(e)(1)].

If it should be determined that for any reason either an insufficient or excessive fee has been paid, please charge any insufficiency or credit any overpayment necessary to ensure consideration of the information disclosure statement for the above-identified application to Deposit Account No. 50-1314. A copy of this paper is enclosed.

Respectfully submitted,

HOGAM

Date: March 17, 2004

Anthony J. Orler

Registration No. 41,232 Attorney for Applicant(s)

500 South Grand Avenue, Suite 1900 Los Angeles, California 90071

Facsimile: 213-337-6701

Telephone: 213-337-6700

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Attorney for Applicant(s)

500 South Grand Avenue, Suite 1900 Los Angeles, California 90071

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FORM PTO-1449			Docket Number (Optional) 81754.0118		Application Number Not assigned	
INFORMATION DISCLOSURE CITATION IN AN APPLICATION			Applicant Terunao HANAOKA			
(Use several sheets if necessary)			Filing Date March 17, 2004		Group Art Unit Not assigned	
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	US 2002/0008320 A1	01/24/02	Kuwabara et al.			
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FOREIGN PATENT DOCUMENTS						
10.4 N	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION (YES/NO)
	WO00/55898	09/21/00	PCT			Abstract
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)						
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.						

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